

FIG. 1
(PRIOR ART)

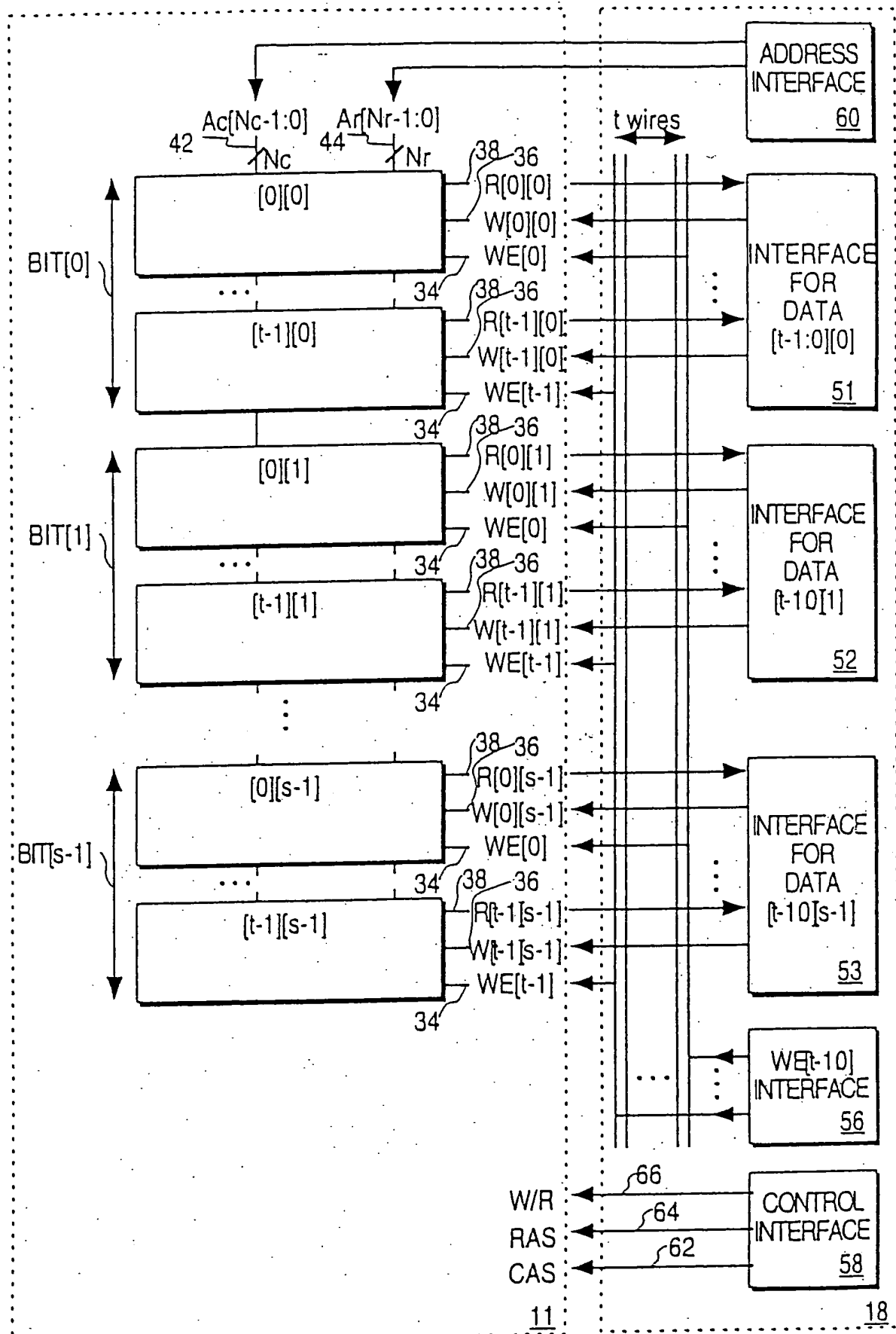


FIG. 2 (PRIOR ART)

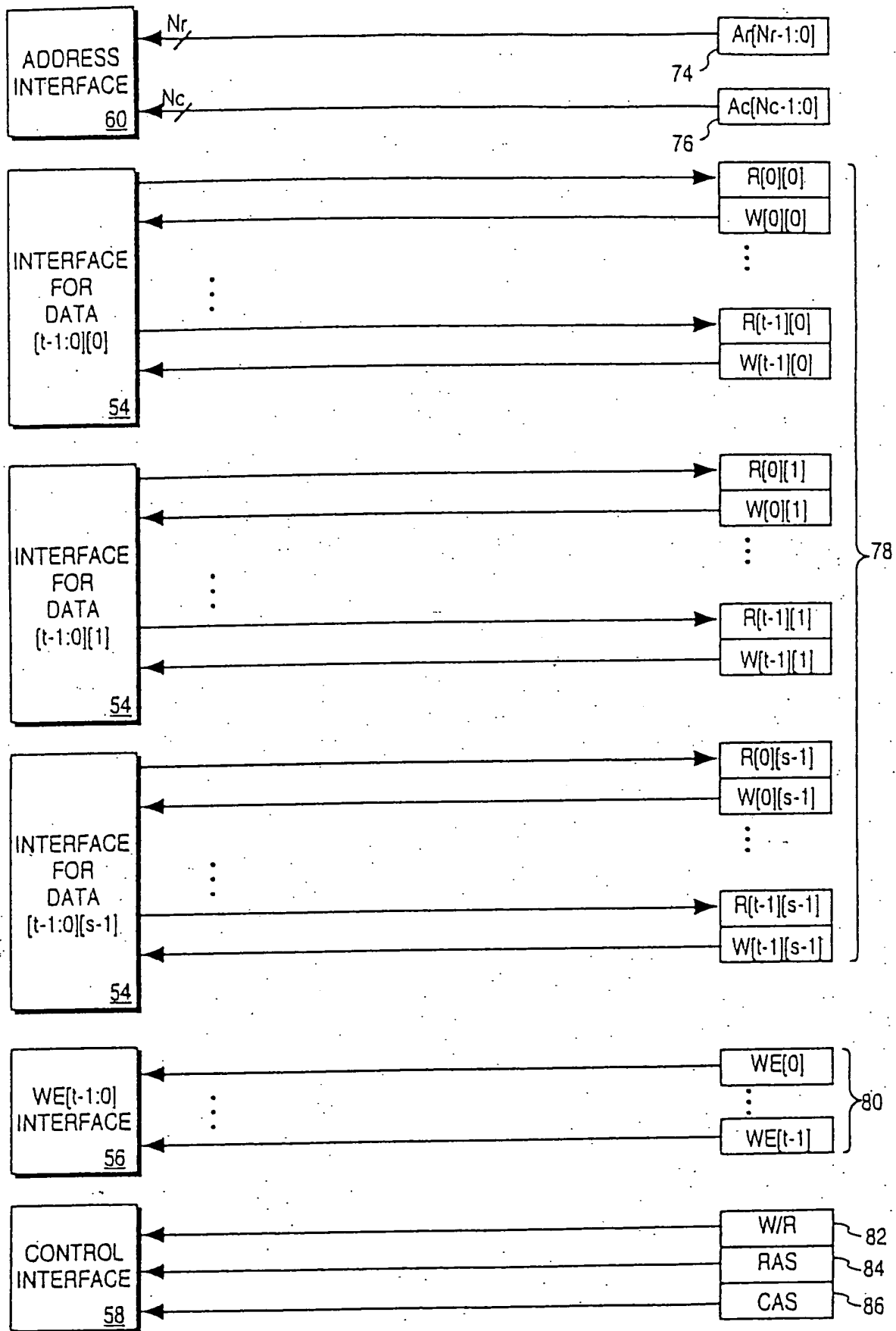


FIG. 3 (PRIOR ART)

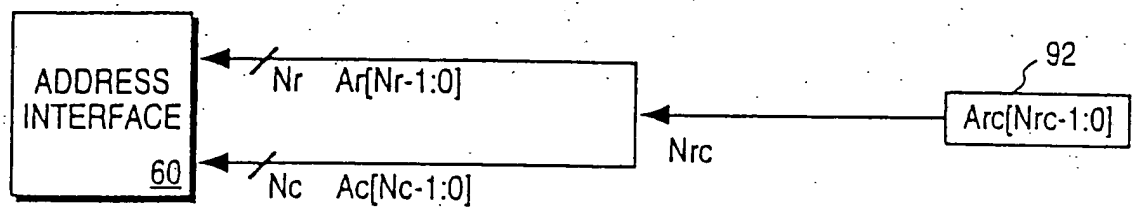


FIG. 4
(PRIOR ART)

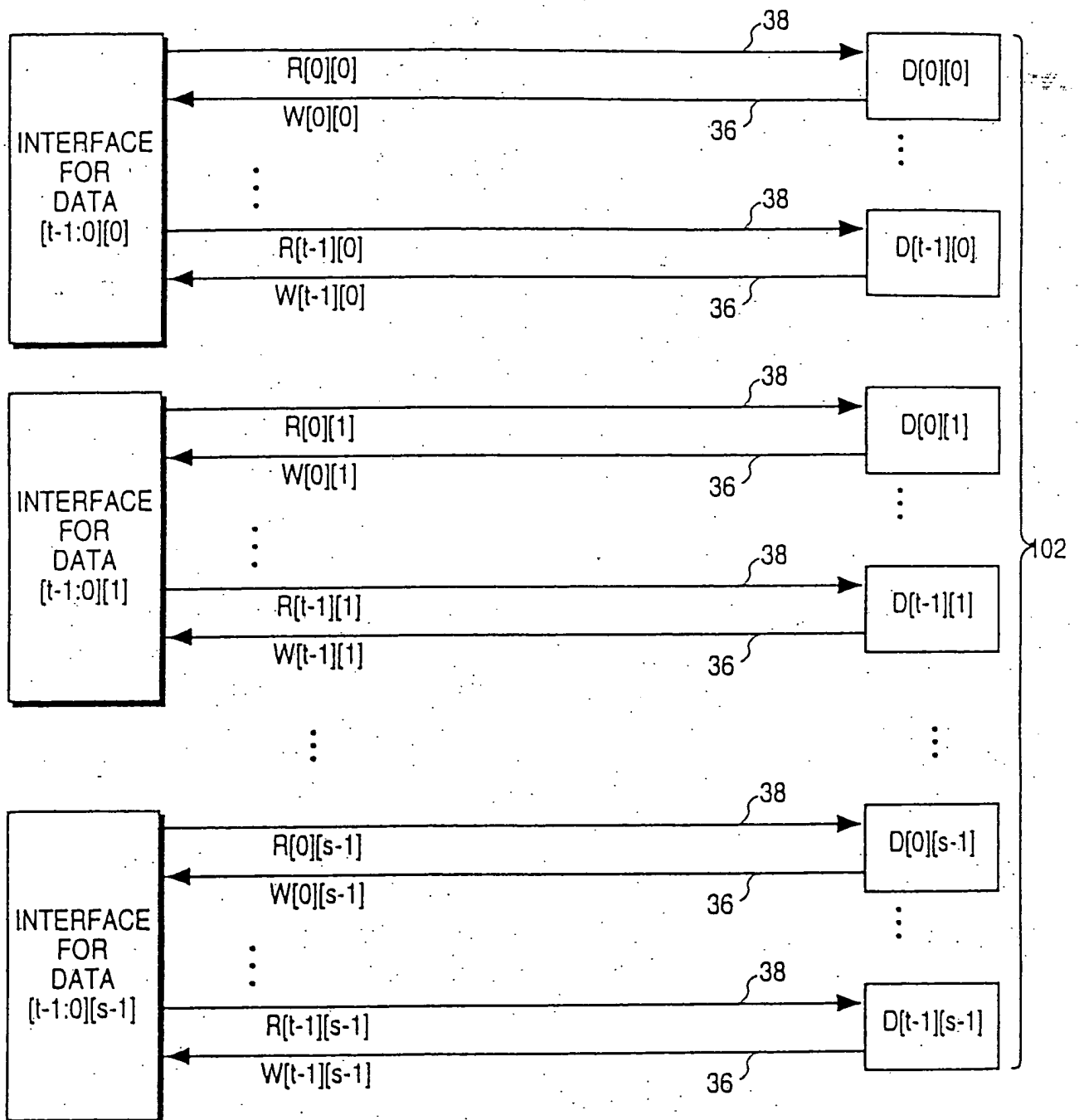


FIG. 5
(PRIOR ART)

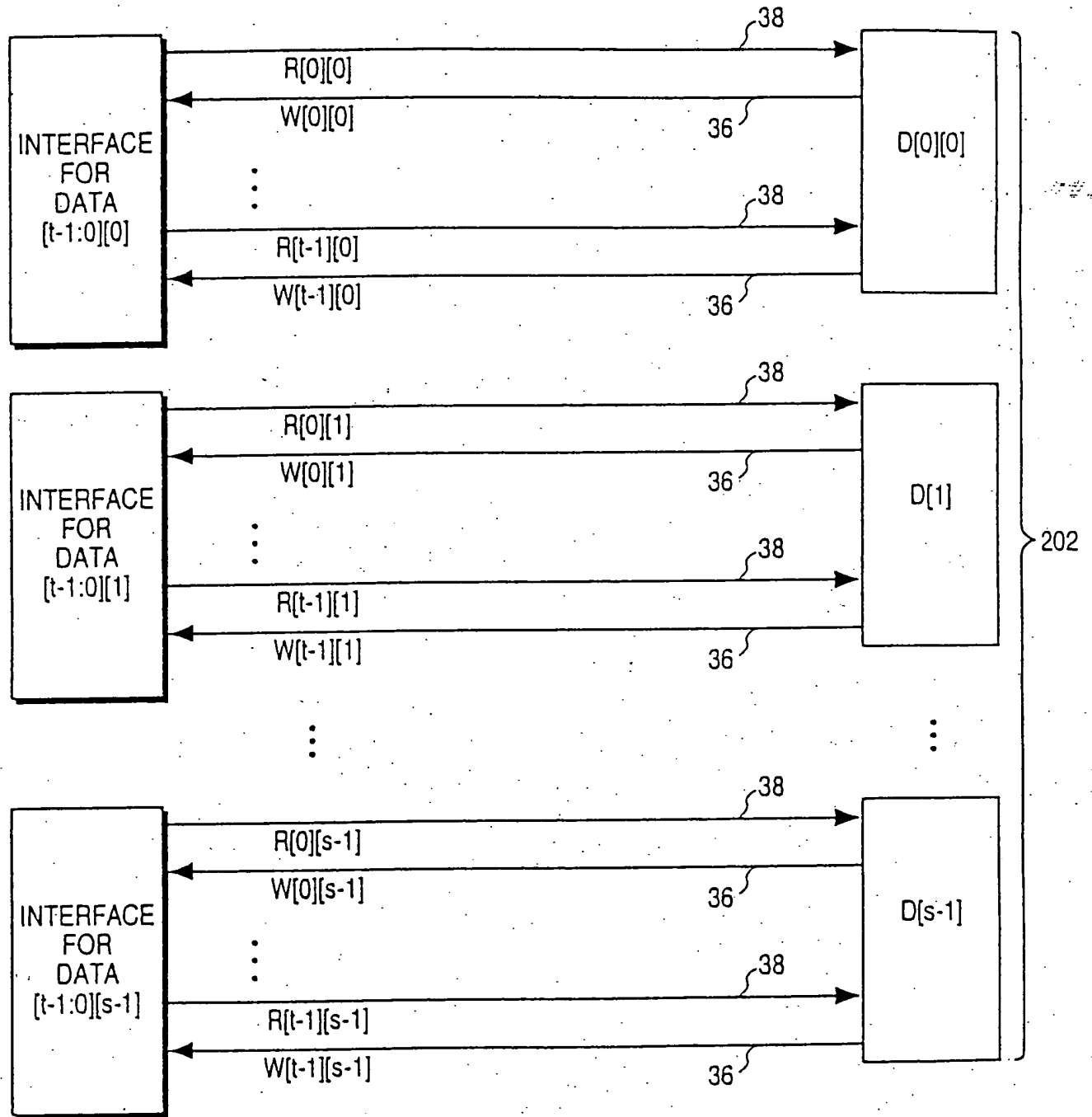


FIG. 6
(PRIOR ART)

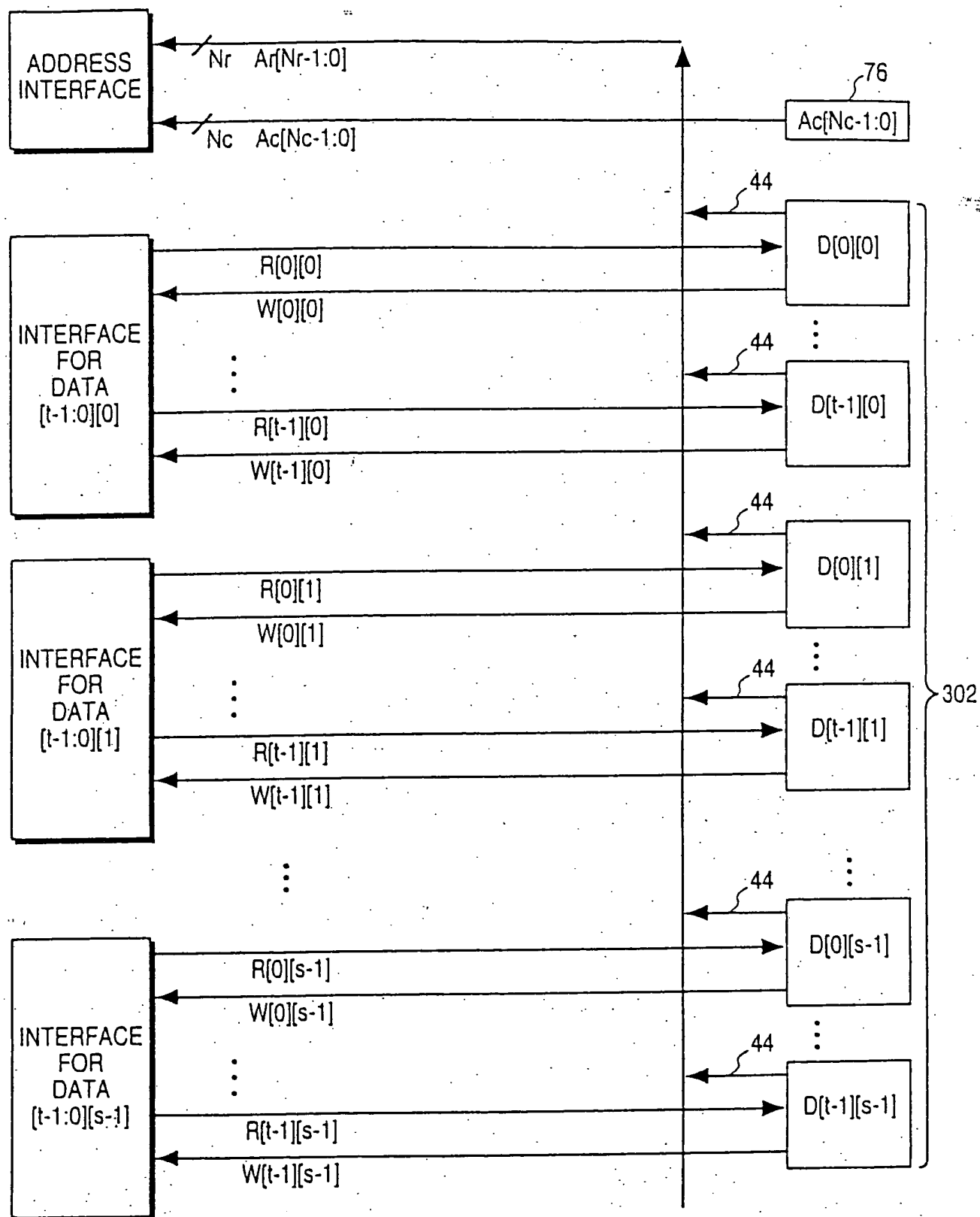


FIG. 7 (PRIOR ART)

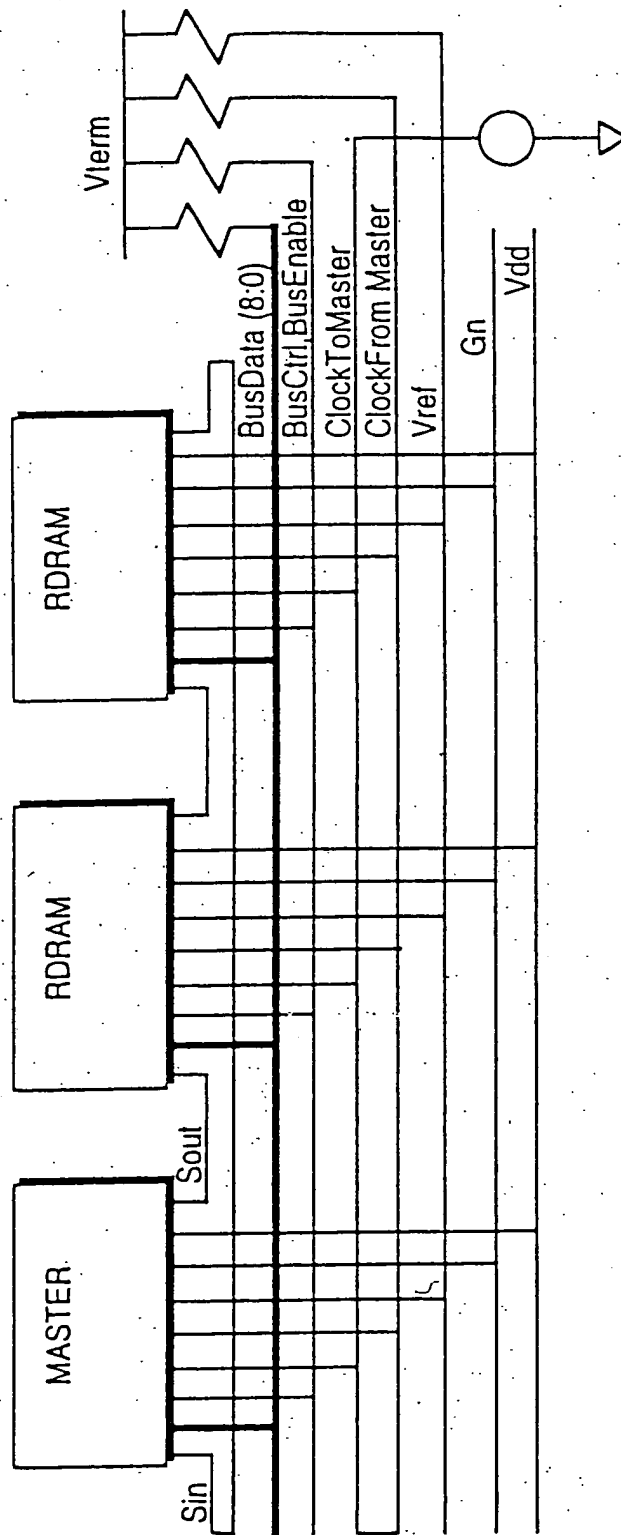


FIG. 8A
(PRIOR ART)

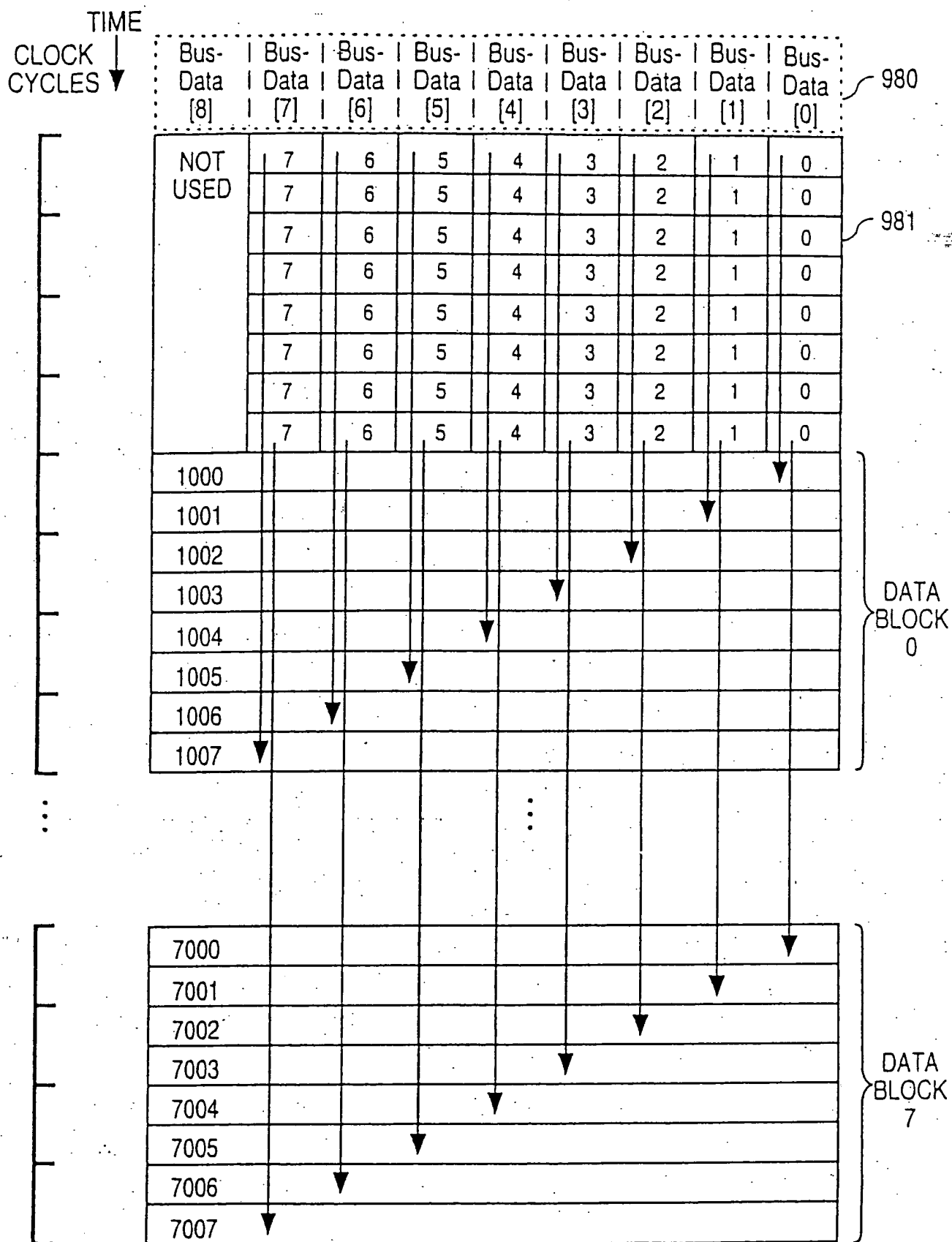


FIG. 8B
(PRIOR ART)

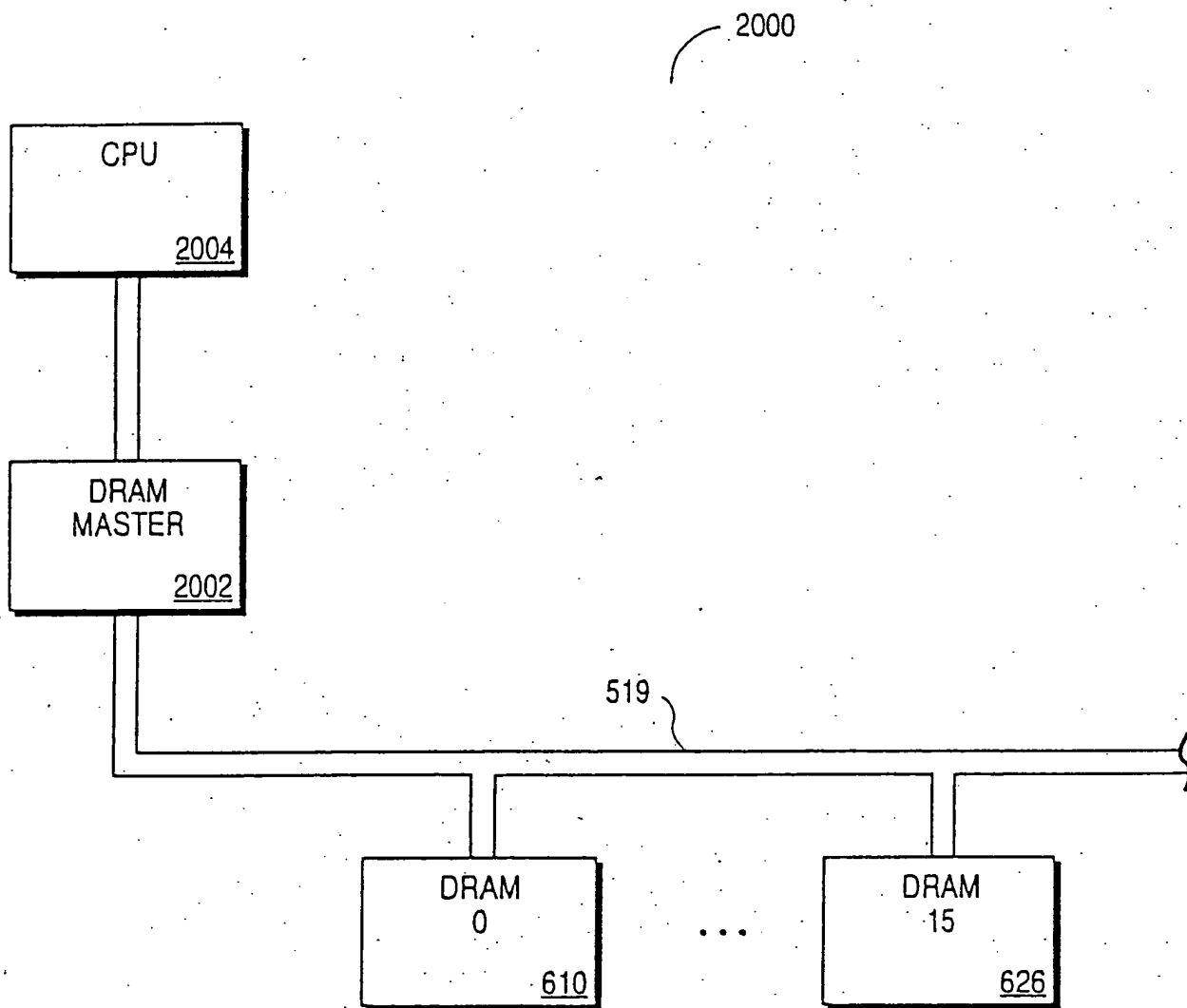


FIG. 9

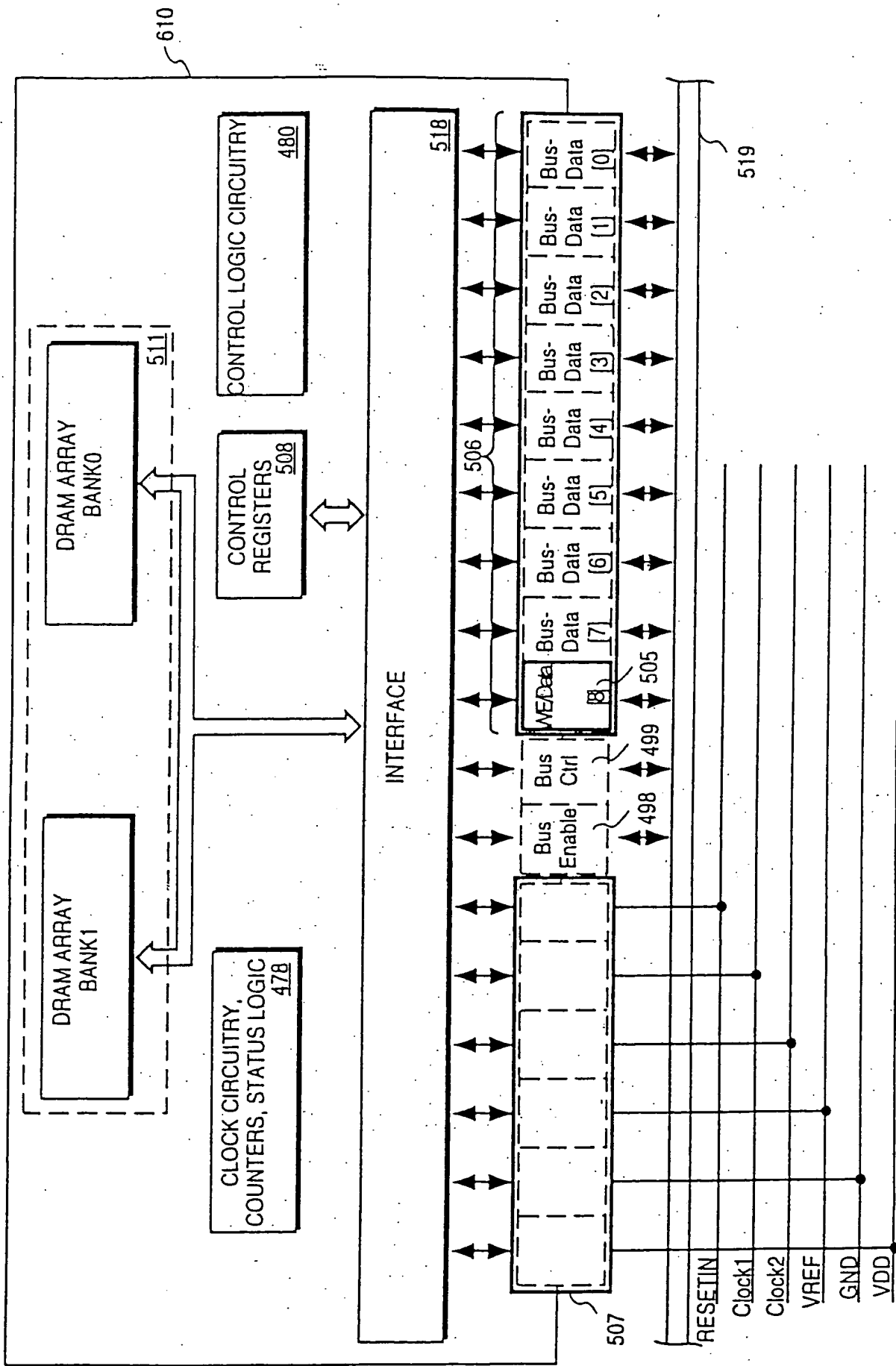


FIG. 10

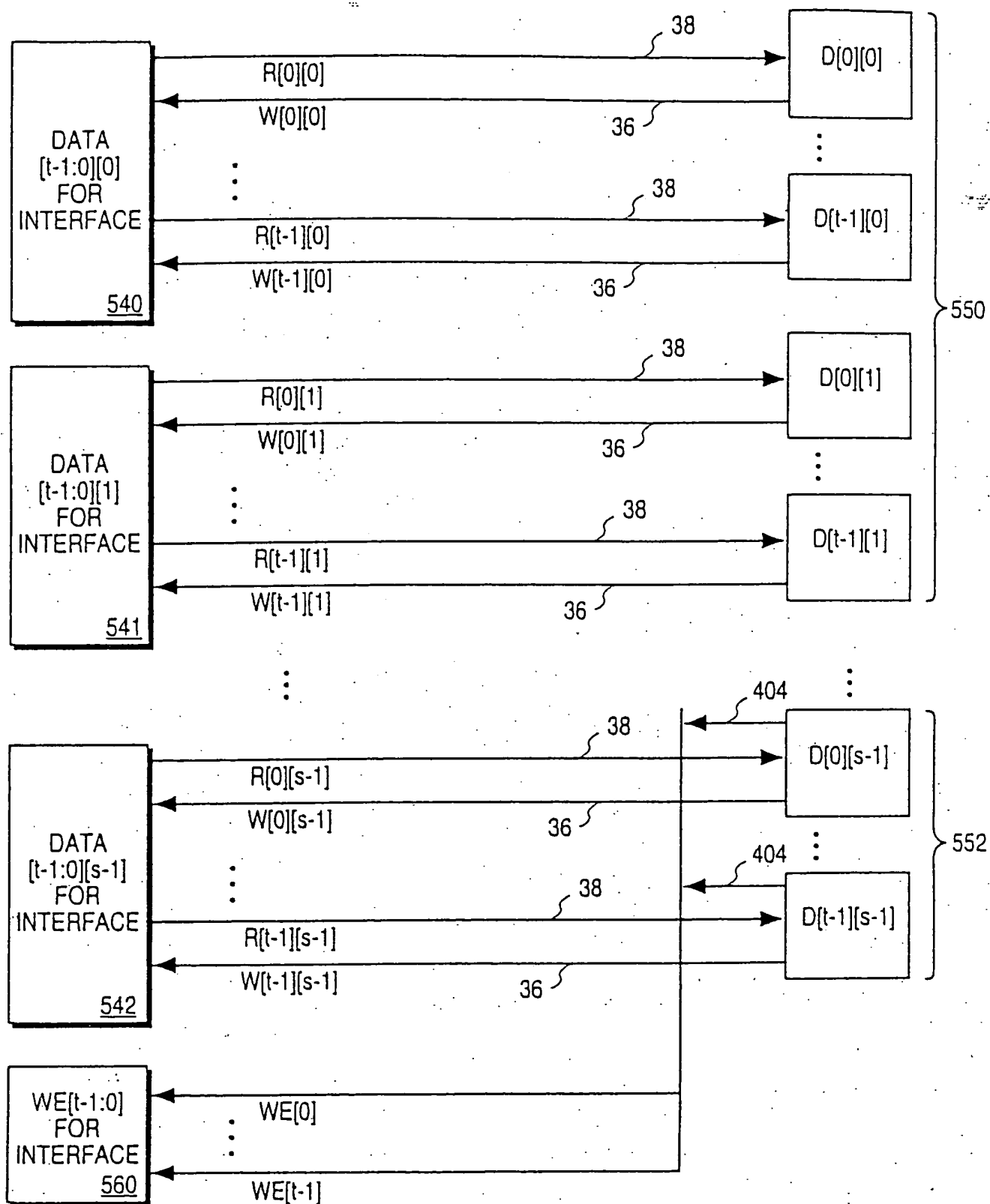


FIG. 11

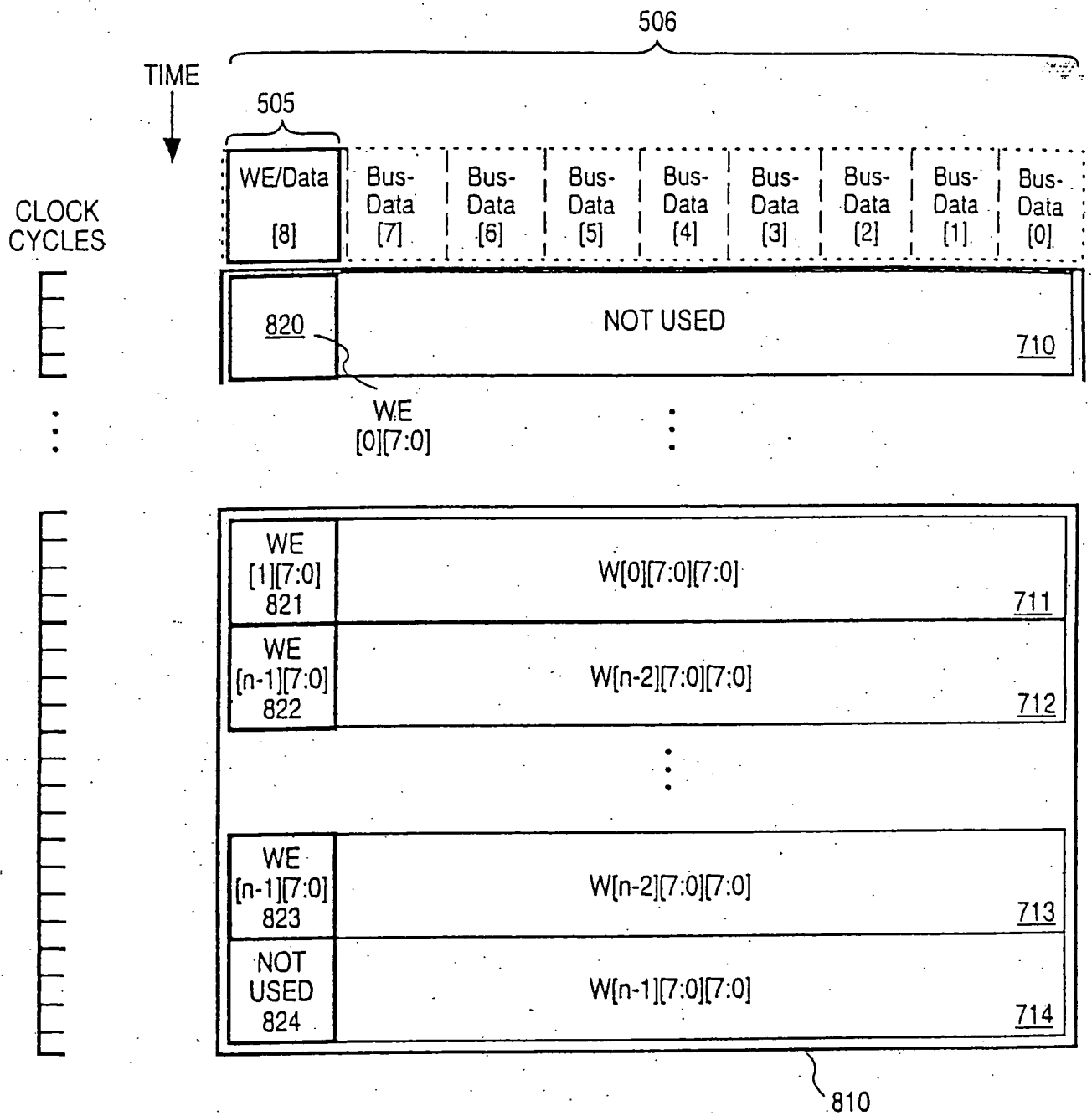


FIG. 12A

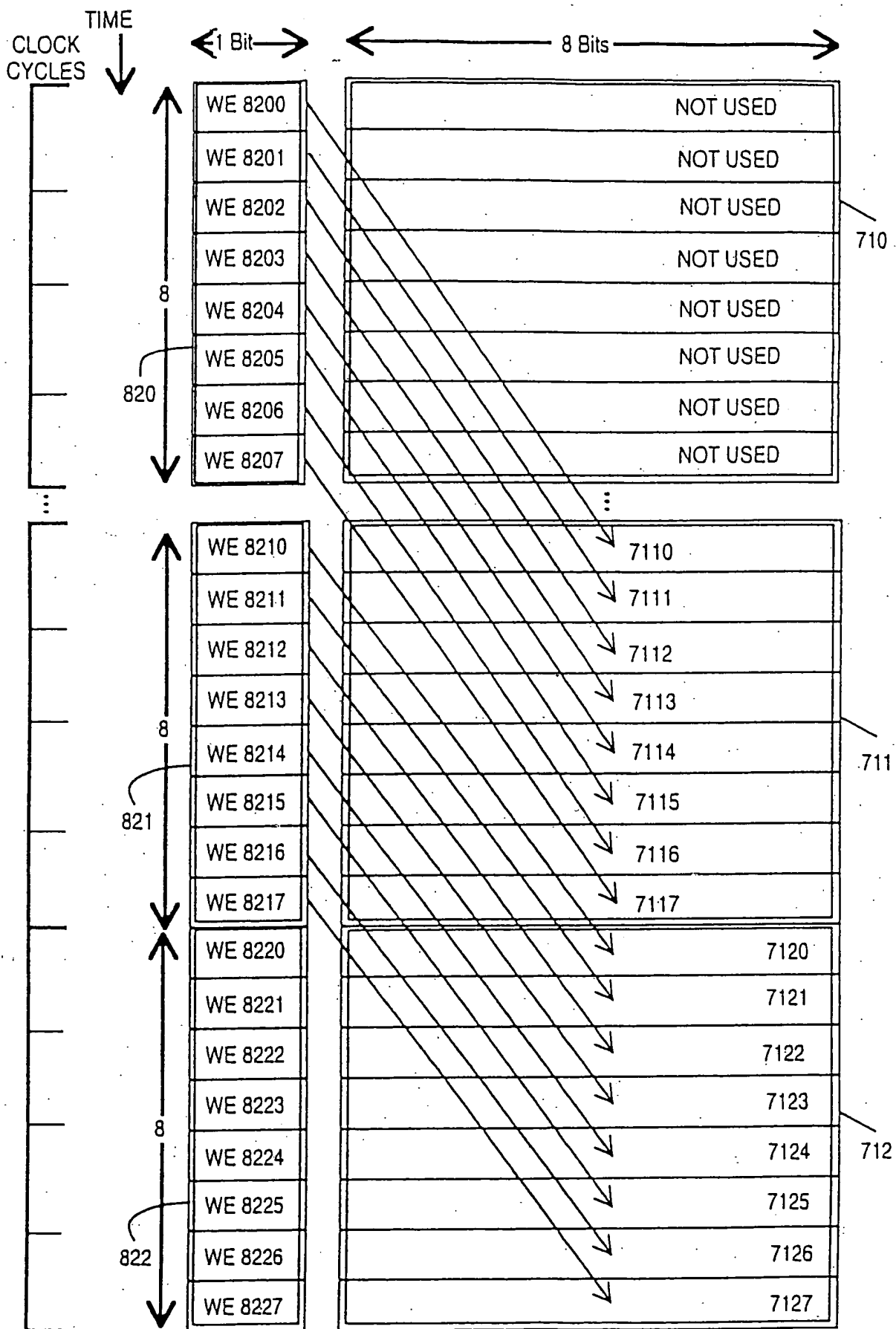


FIG. 12B

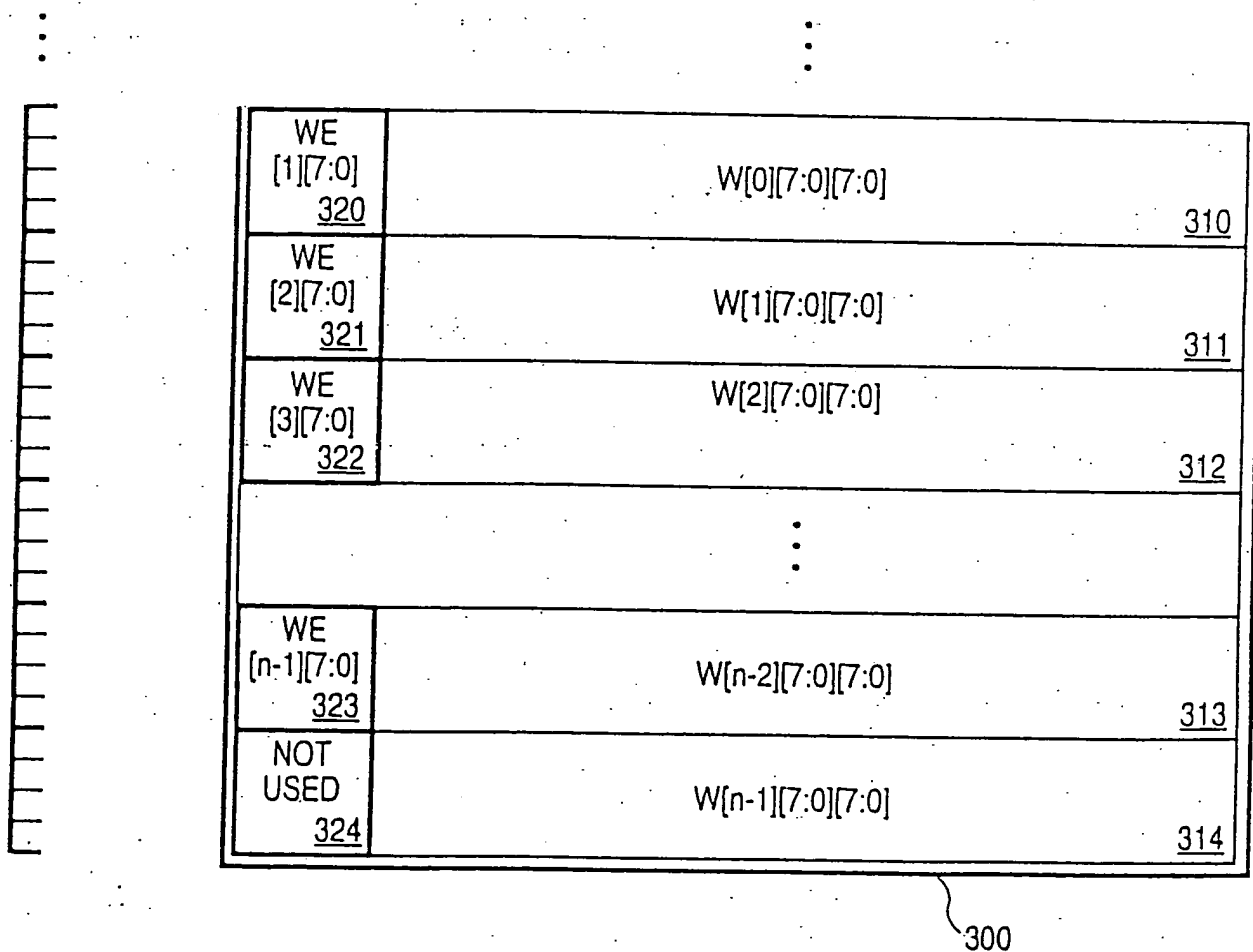
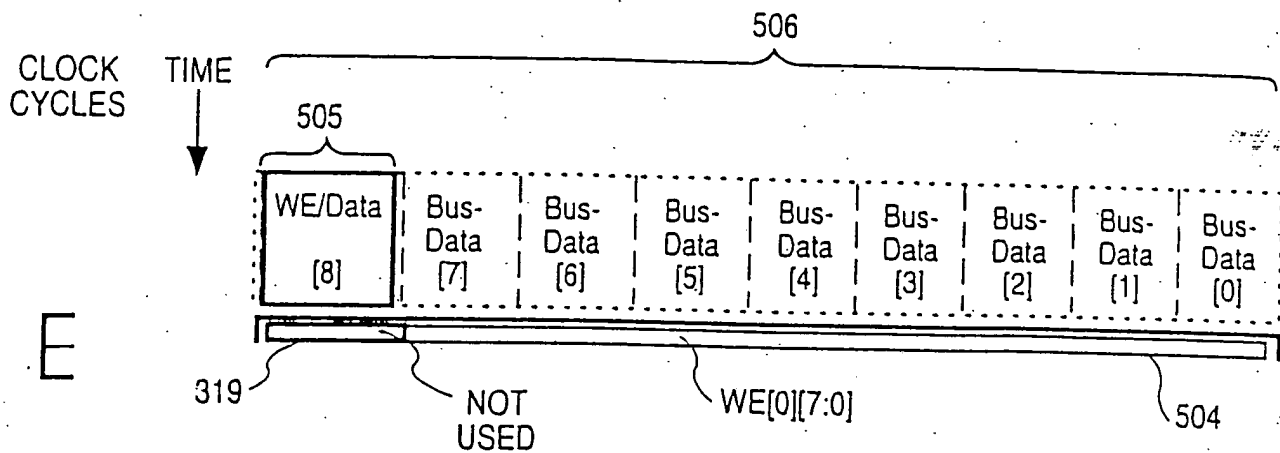


FIG. 13A

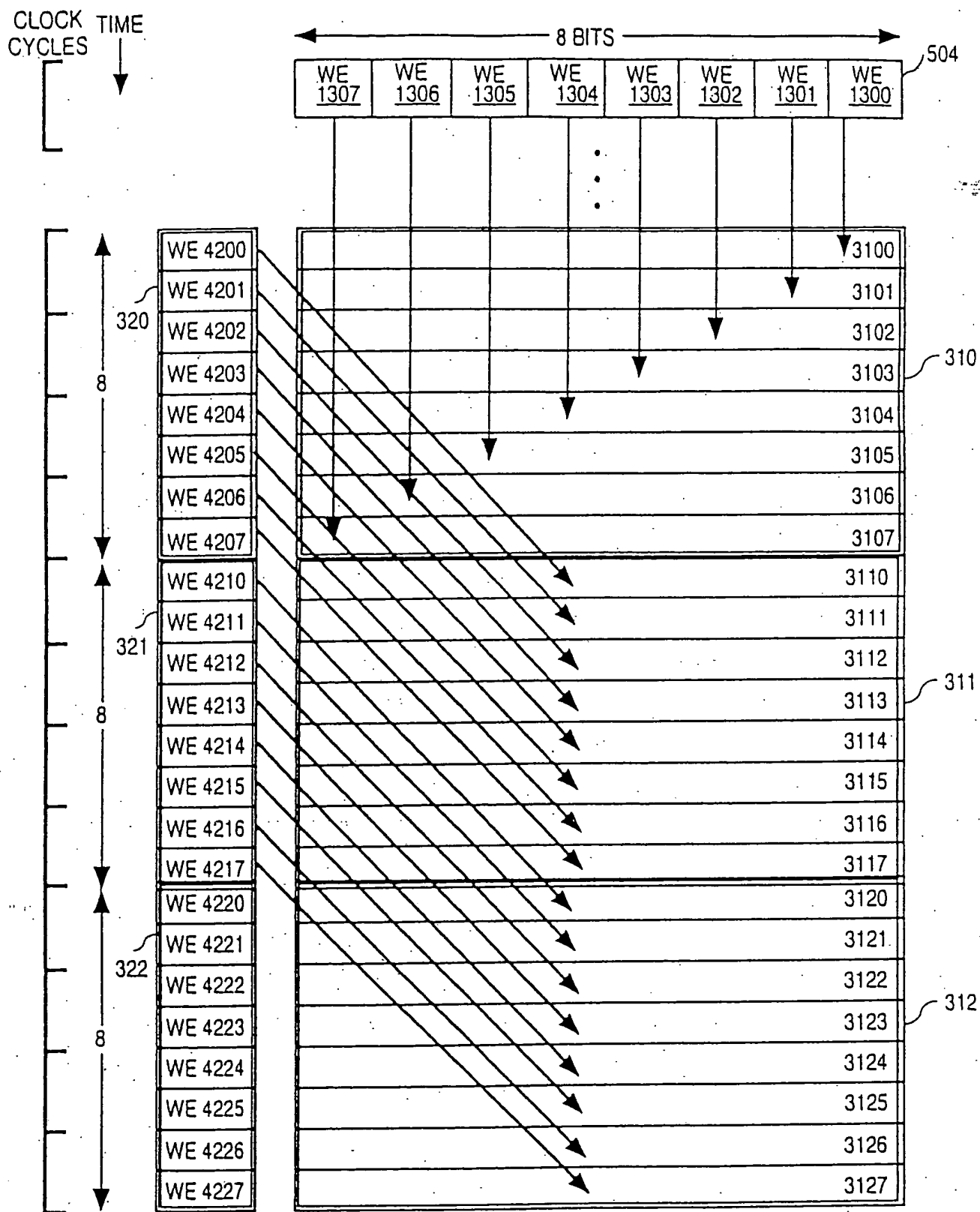


FIG. 13B

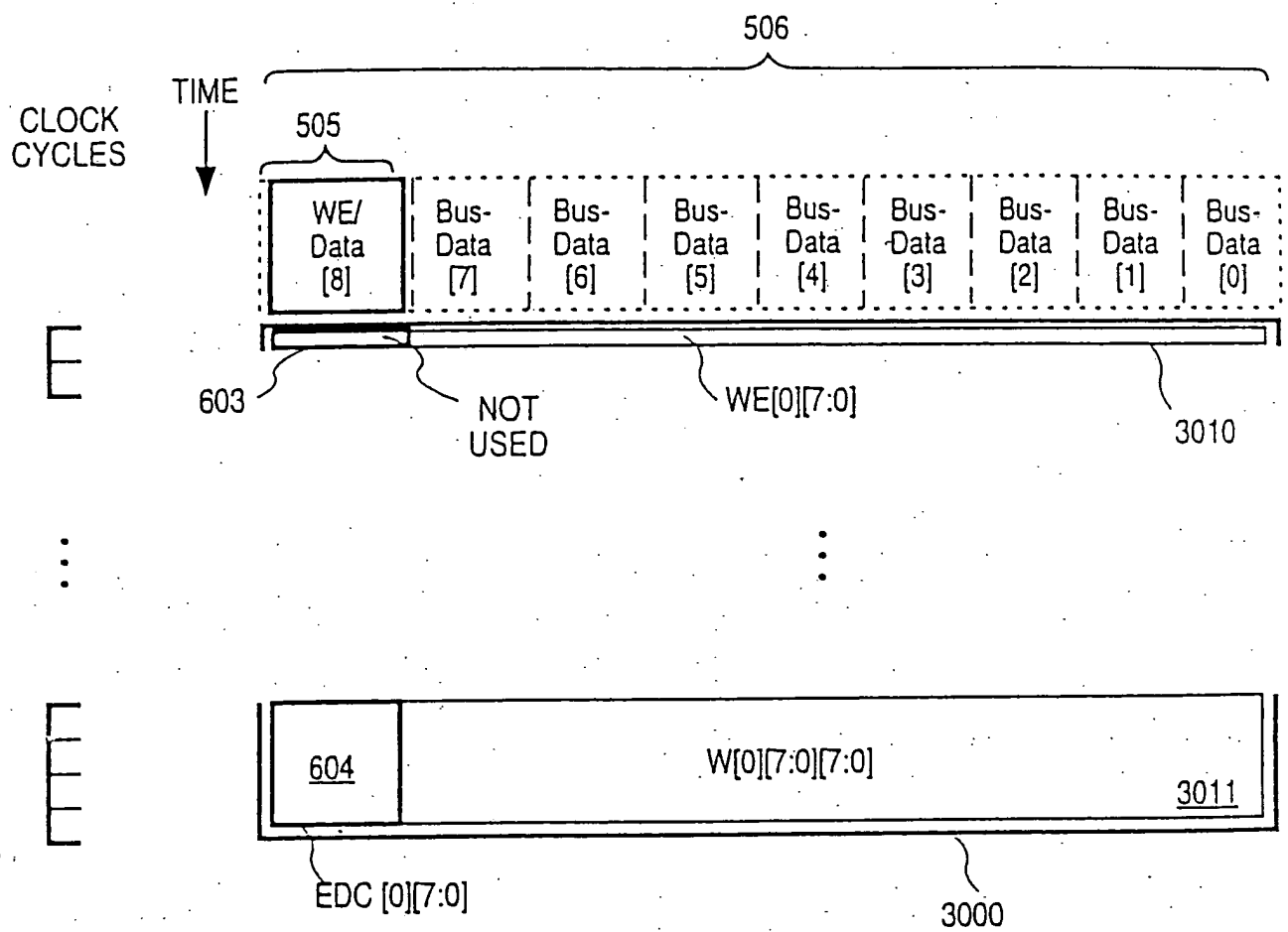


FIG. 14

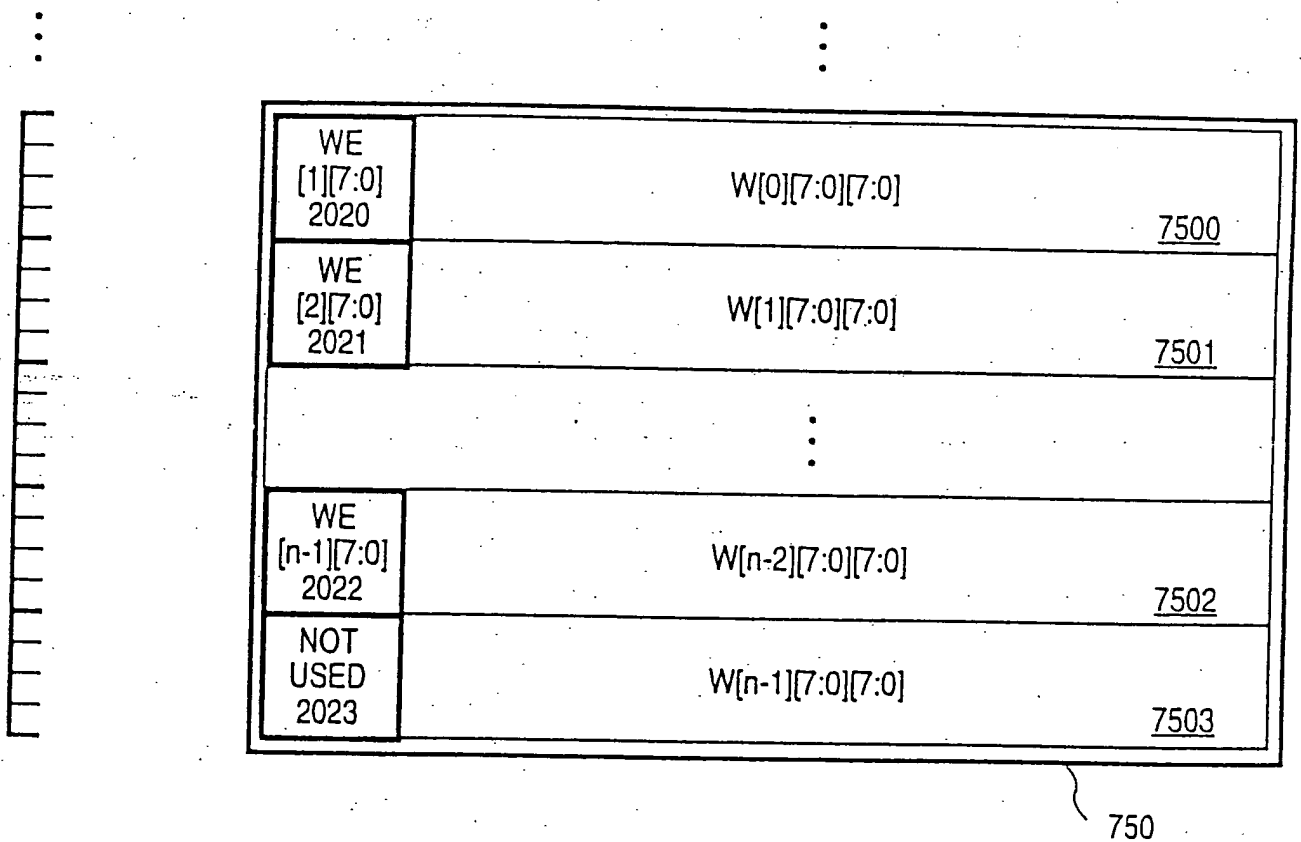
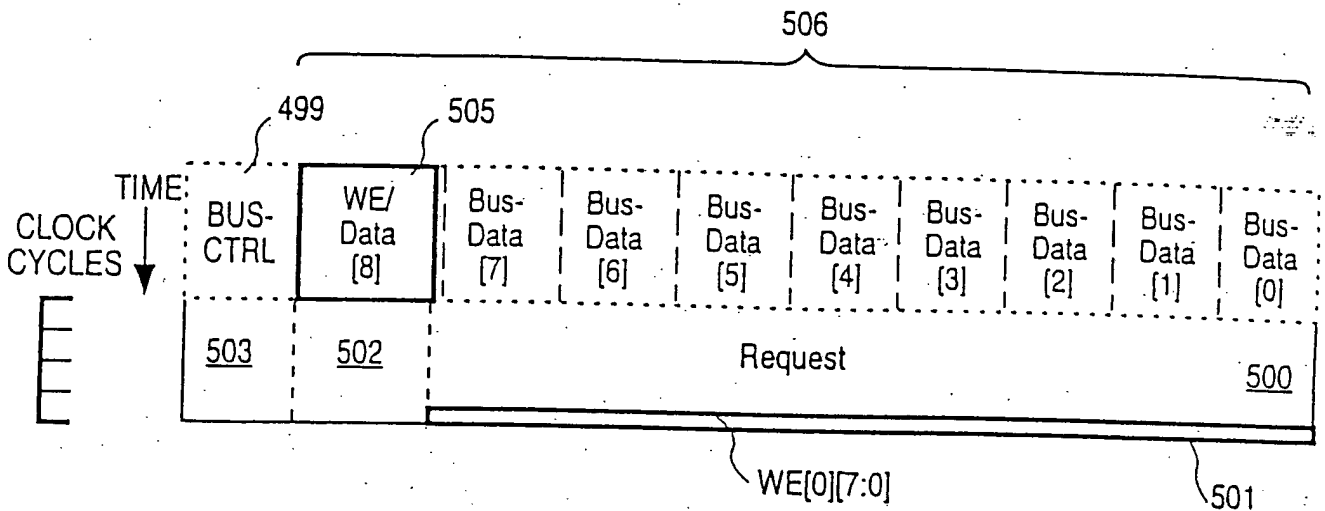
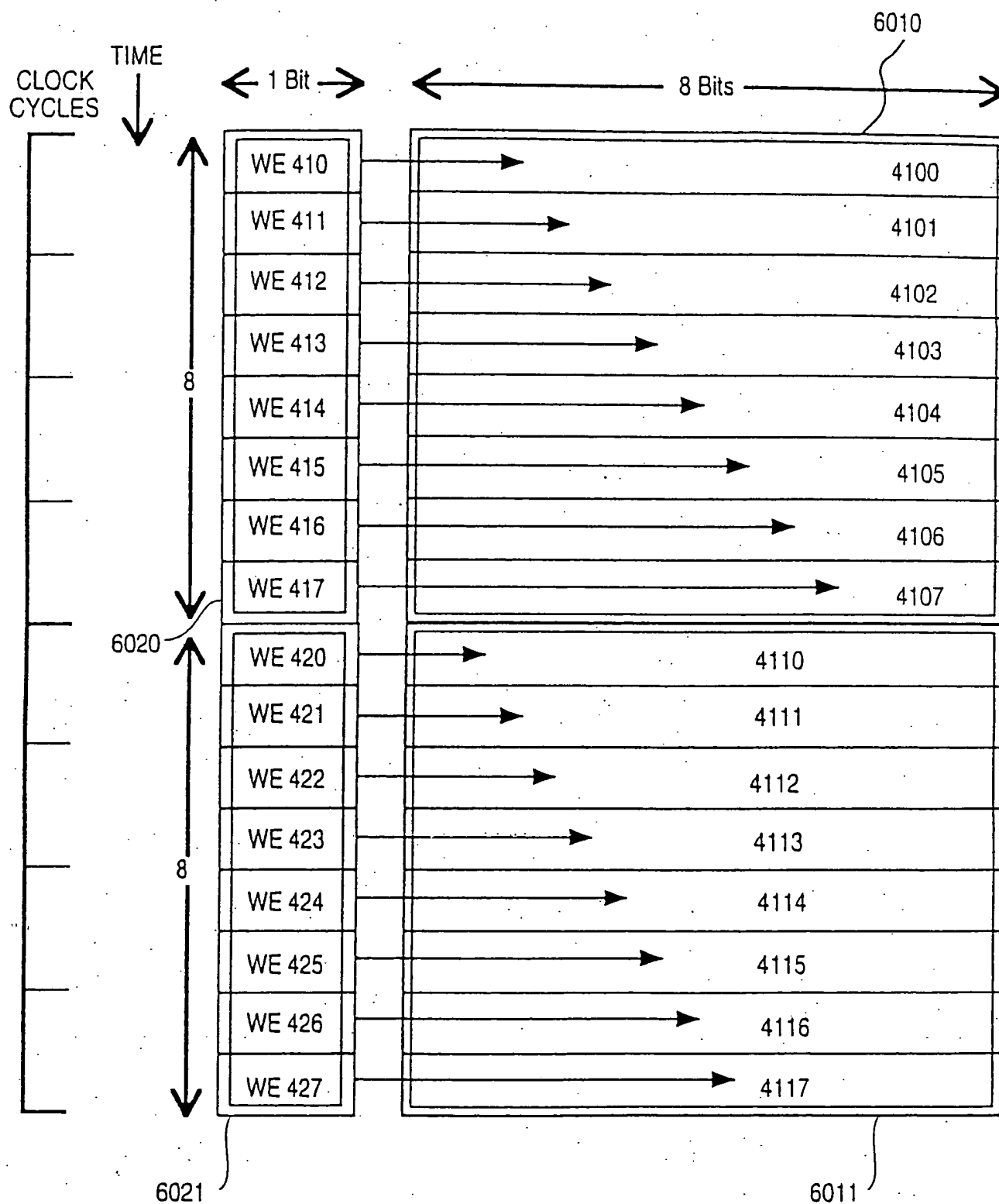


FIG. 15



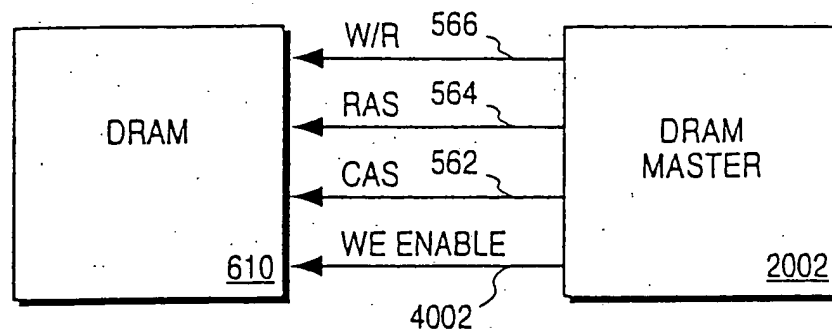


FIG. 17

W/R	RAS	CAS	FUNCTION
0	0	0	CAS READ
0	0	1	CAS WRITE 1
0	1	0	CAS WRITE 2
0	1	1	CAS READ AUTO PRECHARGE
1	0	0	CAS WRITE 1 AUTO PRECHARGE
1	0	1	CAS WRITE 2 AUTO PRECHARGE
1	1	0	PRECHARGE
1	1	1	RAS

FIG. 18